LIST of GeBGO DOCUMENTATION

NAME OF DOCUMENT	FILENAMES	DESCRIPTION
GeBGO Overview	ge_brd.doc	Written description of GeBGO.
L8825-9200-DD-00	gebgoint.sch	Schematic of GeBGO Interface.
L8825-9201-DD-00 (2 pages)	testckt.sch	Schematic of GeBGO Test Circuits.
	testckt.lib	Archived Parts in Schematic
	ge_u30.abl	PAL Programs for GeBGO
	ge_bgo.bom	Bill of Material for GeBGO
	tst_ckt.bom	Bill of Material for Test Circuit.
	vxi_error.c regtest.c ge_memtest.c ge_scaler.c ge_dac.c ge_dac_saw.c ge_lrm.c ma16d16.c wca16d16.c rca16d16.c dump_fifo.c	Test programs for GeBGO
	startup.cmd	Startup Program for VXI
VXI Interface FPGA	vxi_inf.spc	Written description of VXI Interface FPGA
L8825-9202-DB-00	xil_vxi.sch vxi_mux.sch vxi_regs.sch hexinv1.sch hexinv2.sch vxi_out.sch vxi_in.sch	Schematic of VXI Interface (7 pages total)
	xil_vxi.lib	Archived Parts in Schematic
	xil_vxit.mcs	Hex code used to program serial PROM

FASTOUT Data Controller FPGA L8825-9203-DB-00	fastout.doc rls.sch fastadd.sch ram.sch fos.sch decode.sch rls.lib rlsy.mcs	Written description of Fastout FPGA. Schematic of Fastout (5 pages total) Archived Parts in Schematic Hex code used to program serial PROM
SIO FPGA L8825-9204-DB-00	siochip.doc ser_conv.sch p-s_conv.sch s-p_conv.sch mux.sch ser_conv.lib sioa.mcs	Written description of Serial I/O FPGA. Schematic of Serial I/O (4 pages) Archived Parts in Schematic Hex code used to program serial PROM.
SCALER FPGA L8825-9205-DB-00	scaler.spc scaler.sch c16ripp2.sch hx244out.sch xilinxot.sch scaler.lib scalera.mcs	Written description of Scaler FPGA. Schematic of Scaler (4 pages) Archived Parts in Schematic Hex code used to program serial PROM.

DATA SHEETS FOR GeBGO BOARD	ge_bgo.bom XC3042-70-PC84C XC1736A IDT7133 IDT73200 DS3892 DS3890 MC10125 74LS00 74LS08 74 S38 74HC244 74HC374 74LS645	Bill of Materials Xilinx FPGA Xilinx Serial Prom Dual Port Ram (2K x 16) Pipeline Register Octal TRI-STATE Receiver Octal Trapezoidal Driver Quad MECL To TTL Quad NAND Gates Quad AND Gates Quad NAND Open-Collector outputs Octal Buffer Octal D Flip-Flops Octal Bus Transceiver
DATA SHEETS FOR TEST CIRCUIT	tst_ckt.bom 22V10 82S123 DAC8800 74LS00 74LS74 74LS123 74LS165 74LS240 74 S241	Bill of Materials PAL PROM 8 Channel Serial DAC Quad AND Gates Dual D Flip-Flops Monostable Multivibrator Parallel-Load 8-Bit Shift Reg Octal Buffer Octal Buffer